

# **PRODUCT SPECIFICATION**

# 20X4 CHARACTERSLCD MODULE MODEL: C2004A1SBX6B-J0 Ver:1.1

<>> Preliminary Specification

<->Finally Specification

CUSTOME	ER'S APPROVAL
CUSTOMER:	
SIGNATURE:	DATE:

APPROVED	PM	PD	PREPARED
BY	REVIEWED	REVIEWED	BY
2018/4	花件	估.资、	潘远良

# **Revision Status**

Version	Revise Date	Page	Content	Modified By
Ver. 1.0	2017.10.26		First Issued	
Ver. 1.1	2018.01.02		Improve the backlight	

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### 1. Features

The features of LCD are showed as follows

\* Display mode : STN/Yellow-Green/Tranflective/Positive

\* Drive IC : UCI7066-03(English/Russian)

\* Display format : 20\*4Characters \* Interface Input Data : 8 Bit or 4Bit MPU \* Driving Method : 1/16Duty, 1/4 Bias

\* Viewing Direction : 6 O'clock

\* Backlight : LED/ Yellow-Green

\*Sample NO. : C2004A1SBX6B-J0\_01/20171226

### 2. MECHANICAL SPECIFICATIONS

Item	Specification	Unit
Module Size	146(W) x62.5(H) x 13.6MAX(T)	mm
View display area	123.5(W) x43 (H)	mm
Activity Area	118.84 (W) x38.47(H)	mm
Character Font	5x8dots	-
Character Size	4.84W) x 9.22 (H)	mm
Character Pitch	6.00(W)x9.75(H)	mm
Dot size	0.92(W) x1.10(H)	mm
Dot Pitch	0.98(W) x 1.16(H)	mm

### 3. ELECTRICAL SPECIFICATIONS

### **3-1 ABSOLUTE MAXIMUM RATINGS**

Item	Symbol	Stand			
item	Symbol	Min.	Тур.	Max.	Unit
Supply Voltage For Logic	VDD – VSS	-0.3	-	7	V
upply Voltage For LCD Drive	V <sub>LCD</sub> = V <sub>DD</sub> - V <sub>0</sub>	V <sub>DD</sub> -10	-	V <sub>DD</sub> +0.3	V
Input Voltage	Vin	-0.3	-	VDD+0.3	V
Operating Temp.	Тор	-20	-	+70	°C
Storage Temp.	Tst	-30	-	+80	°C

<sup>\*.</sup> NOTE: The response time will be extremely slow when the operating temperature isaround -10°C, and the back ground will become darker at high temperature operating.

### **3-2 ELECTRICAL CHARACTERISTICS**

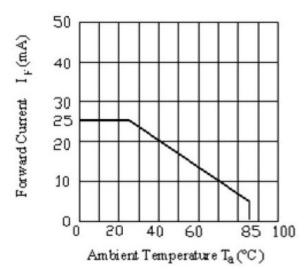
Item		Symbol	Test Condition	Min.	Тур.	Max.	Unit
Logic supply Voltage		V <sub>DD</sub> – V <sub>ss</sub>		4.5	5	5.5	V
LCD Drive Voltage		$V_{LCD}$		4.0	4.3	4.6	V
	"H" Level	V <sub>IH</sub>	Ta = 25 °C	0.7VDD	-	VDD	V
Input Voltage	"L" Level	V <sub>IL</sub>	V <sub>DD</sub> =5V ± 10%	-0.3	-	0.55	V
Frame Frequency		f <sub>FLM</sub>		-	84.4	-	Hz
Current Cons	umption	I <sub>DD</sub>		-	2.8	-	mA

### 3-3BACKLIGHT3-3-1. Absolute Maximum Ratings

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Forward Current	lfm		-	-	60*2	mA
Reverse Voltage	VR	Ta=25 °C	-	-	5	V
Power Dissipation	PD		-	-	103.5*2	mW

### 3-3-2. Electrical-optical Characteristics

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Forward Voltage	VF		2.8	3.0	3.2	V
Average Luminous Intensity (measured without LCD panel)	lv	If=90mA Ta=25°C	400	600	-	cd/m2
Peak wave length	λР		569	572	575	nm



The brightness is measured without LCD panel

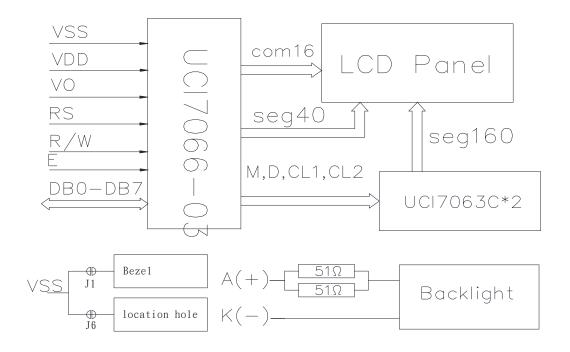
For operation above 25 °C,Thelfm&Pd must be derated , the current derating is -0.36\*3mA/°C for DC drive and -0.86\*3mA/°C for Pulse drive ,the Power dissipation is -0.75\*3mW/°C.The product working current must not more than the 60% of the lfm or lfp according to the working temperature.

## 4. TERMINAL FUNCTIONS AND BLOCK DIAGRAM

### **4-1INTERFACE PIN FUNCTION DESCRIPTION**

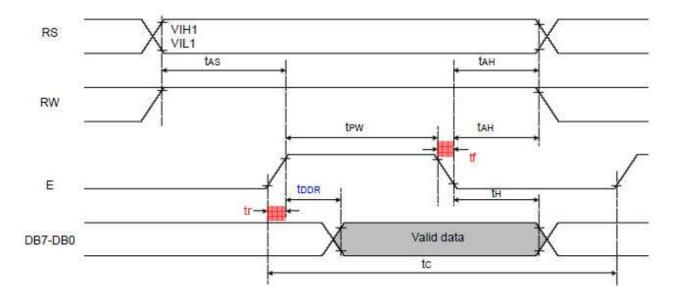
PIN NO.	SYMBOL	FUNCIONS
1	VSS	Ground
2	VDD	Supply voltage for logical circuit
3	V0	Supply voltage for LCD driving
4	RS	A signal for selecting registers.  1: Data Register (for read and write)  0: Instruction Register (for write)
5	R/W	A signal for selecting read or write actions.1: Read, 0: Write.
6	E	A enable signal for reading or writing data.
7-14	DB0~DB7	8 Bit Data Bus
15	Α	Backlight (+5.0V)
16	K	Backlight (-)
17	NC	No connect
18	NC	No connect

### **4-2BLOCK DIAGRAM**

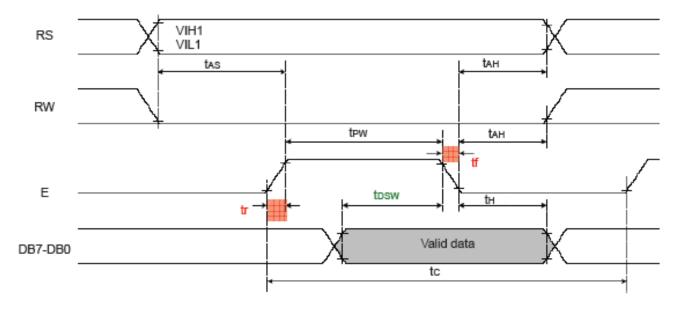


# **5. TIMING CHARACTERISTICS**

### 5-1 Reading data from UCI7066U to MPU

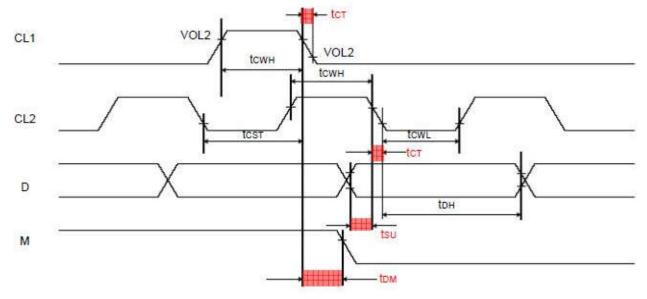


## 5-2 Writing data from MPU to UCI7066U

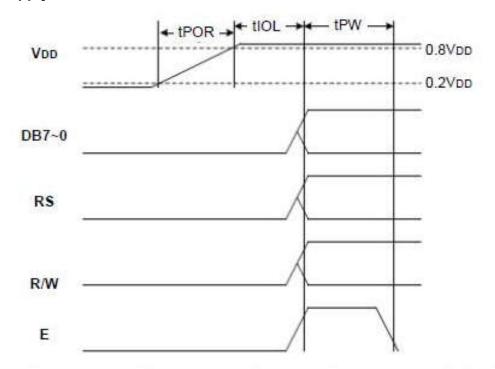


(6800 Write data to UCi7066c)

### 5-3 With External Driver



# **5-4 Power Supply Conditions**



Symbol	Characteristics	Description	Min	Typ.	Max.	Unit
tPOR	Power Rise time	Power rise time that will trigger internal POR circuit	0.1		100	mS
tIOL	I/O Low time	The period that I/O is kept LOW	40			mS
tPW	Enable Pulse width	Please refer to the following	tables			•

# 5-5 Parameter

Ta = 25°C, Vcc=4.5V~5V

Symbol	Characteristic	Test Condition	Min.	Тур.	Max.	Unit
Internal Clock	k Operation		2	8		V.
fosc	OSC Frequency	R=91ΚΩ	190	270	350	KHz
External Cloc	k Operation	Y.		191.755	v.	
fEX	External Frequency	22	125	270	410	KHz
	Duty Cycle	<del>2</del> 0	45	50	55	%
tr, tr	Rising/Falling Time	2	22	72	0.2	uS
Write Mode (I	MPU writes data to UCi7066)	***	*	Vir	V	()); //e
tc	Enable Cycle Time	Pin E	1200	-	-	nS
tpw	Enable Pulse Width	Pin E	140	-	<u> </u>	nS
tr, tr	Rising/Falling Time	Pin E	-	-	25	nS
tas	Address Setup Time	Pin: RS, RW, E	0	-	-	nS
tah	Address Hold Time	Pin: RS, RW, E	10	15	-	nS
tosw	Data Setup Time	Pin: DB7~DB0	40	-	-	nS
tн	Data Hold Time	Pin: DB7~DB0	10	-	-	nS
Read Mode (I	MPU reads data from UCi706	6)	58	()(	)!: N:	as
tc	Enable Cycle Time	Pin E	1200	-	-	nS
tpw	Enable Pulse Width	Pin E	140	-	-	nS
tr, tr	Rising/Falling Time	Pin E			25	nS
tas	Address Setup Time	Pin: RS, RW, E	0	+	-	nS
tah	Address Hold Time	Pin: RS, RW, E	10	. 85	-	nS
toor	Data Setup Time	Pin: DB7~DB0	-	1	100	nS
tн	Data Hold Time	Pin: DB7~DB0	10	-	-	nS
Interface Mod	le with LCD Driver (UCi7065)			(1)		
tcwn	Clock Pulse Width, High	Pin: CL1, CL2	800	-	_	nS
tcwL	Clock Pulse Width, Low	Pin: CL1, CL2	800	844	=	nS
tcsT	Clock Setup Time	Pin: CL1, CL2	500	1	-	nS
tsu	Data Setup Time	Pin: D	300	7/2	Ξ.	nS
ton	Data Hold Time	Pin: D	300	1	-	nS
tom	M Delay Time	Pin: M	0	925	2000	nS

# **6.COMMAND LIST**

The following is a list of host commands supported by UCi7066

R/S: 0: Control, 1: Data W/R: 0: Write Cycle, 1: Read Cycle D7-D0: -: Don't Care

#	Command	RS	R/W	D7	D6	D5	D4	D3	D2	D1	D0	Action
1	Clear Display	0	0	0	0	0	0	0	0	0	1	Clear the screen
2	Return Home	0	0	0	0	0	0	0	0	1		Move cursor to HOME
3	Set Entry Mode	0	0	0	0	0	0	0	1	I/D	S	I/D: Left / Right S: Shift OFF/ON
4	Display ON/OFF	0	0	0	0	0	0	1	D	С	В	D: Display OFF / ON C: Cursor OFF / ON B: Blink OFF / ON
5	Cursor or Display Shift	0	0	0	0	0	1	S/C	R/L	=	2	S/C: Screen / Cursor R/L Right / Left
6	Set Function	0	0	0	0	1	DL	N	F			DL: 4-bit / 8-bit, N: 1-line / 2-line F: 5x8 / 5x11
7	Set CGRAM address	0	0	0	1	AC5	AC4	AC3	AC2	AC1	AC0	
8	Set DDRAM address	0	0	1	AC12	AC11	AC10	AC9	AC8	AC7	AC6	
9	Read Busy Flag and address	0	1	BF	AC19	AC18	AC17	AC16	AC15	AC14	AC13	
10	Write data to RAM	1	0	D7	D6	D5	D4	D3	D2	D1	D0	Write data to RAM
11	Read data from RAM	1	1	D7	D6	D5	D4	D3	D2	D1	D0	Read data from RAM
		X		X	For	\$8/\$9	Mode	Vi		0	X	
12	Status Read	1	1	0	0	0	0	0	0	0	0	Read status
12	Status Read	0	1	BF	AC19	AC18	AC17	AC16	AC15	AC14	AC13	The second secon

### Note:

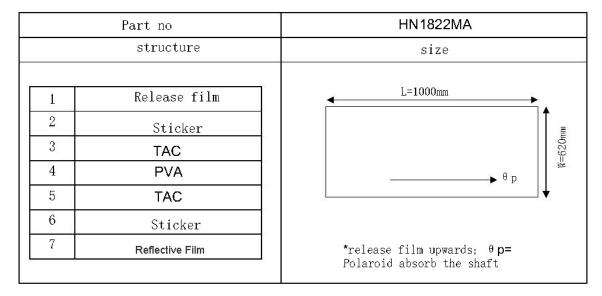
Ensure that UCi7066 is not in the BUSY state (BF = 0) before sending an instruction from the MPU to the UCi7066. If an instruction is sent without checking the busy flag, the time between the first instruction and next instruction will take much longer than the instruction time itself.

# 7. CHARACTER GENERATOR ROM

No. 7066-03

															o. 70t	
Upper 4 bits Lower 4 bits	0000	0001	0010	0011	0100	0101	0110	0111	1 000	1 001	1010	1011	11 00	11 01	1110	1111
0000																
0001																
0010																
0011																
0100																
0101																
0110																
0111																
1000																
1001																
1010																
1011																
1100																
1101																
1110																
1111																

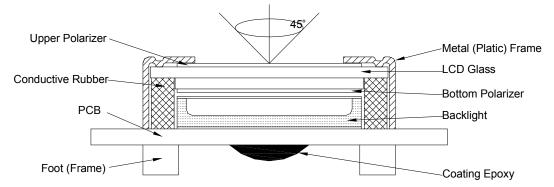
# 8. Polaroid



V-	item		unit	spec value	notes
	length		mm	1000(±10)	
size	width		mm	620(±10)	
	effective thicknes	s	ищ	280±10%	
thickness	Release film		μM	38±10%	
Ctrin Fares	To glasses		gf/25mm	Above 500g	
Strip Force	Release fil	.m	gf/25mm	Under 20g	
	Monomer transmitt	ance	%	42.0±1.5	The original panel data
	Rectangular transmi	ttance	%	≤2.0	The original panel data
Transmittance	380nm transmitta	ince	%	≤1.0	
	Cutting Angle		٥	90 ± 1.0	
Angle	Absorption of shaft	Angle	٥	±2.0	
	BENDING		mm	≤±50	
	L		NBS	$65.0 \pm 2.0$	The original panel data
Hue	a		NBS	-1.97±2.0	The original panel data
	b		NBS	2.98±2.0	The original panel data
degree of	polarization		%	≥99.0	The original panel data
apparent d	efect (above Diamet	er 150µm)		<b>≤13/</b> pcs	
80℃	80℃*500Hr (post- testing)Transmit				
60℃*909	%RH*500Hr	omer transmittance c Hue change value≦ aration, no foam, stri	≤3.0		

## 9. QUALITY SPECIFICATIONS

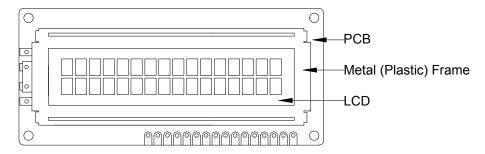
- 9-1. LCM Appearance and Electric inspection Condition
  - 1. Inspection will be done by placing LCM 30cm away from inspector's eyeballs under normal illumination.



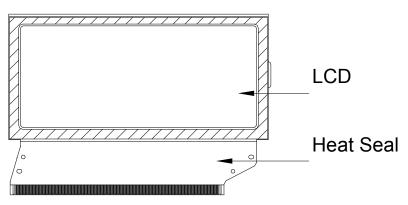
2. View Angle: with in 45° around perpendicular line.

### 9-2. Definition

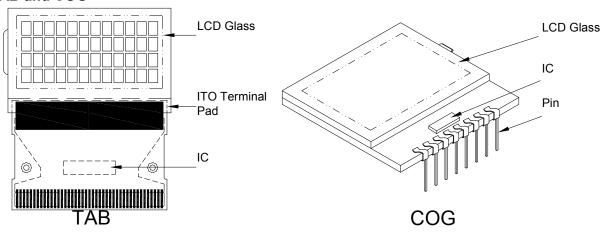
1. COB



2. Heat Seal



3. TAB and COG



## 9-3. Sampling Plan and Acceptance

1.Sampling Plan

MIL - STD - 105E ( || ) ordinary single inspection is used.

2.Acceptance

Major defect: AQL = 0.65% Minor defect: AQL = 1.5%

### 9-4. Criteria

## 1.COB

Defect	Inspection Item	Inspection Standards	
Major	PCB copper flakes peeling off	Any copper flake in viewing Area should be greater than 1.0mm <sup>2</sup>	Reject
Major	Height of coating epoxy	Exceed the dimension of drawing	Reject
Major	Void or hole of coating epoxy	Expose bonding wire or IC	Reject
Major	PCB cutting defect	Exceed the dimension of drawing	Reject

### 2.SMT

Defect	Inspection Item	Inspection Standa	ards
Minor	Component marking not readable		Reject
Minor	Component height	Exceed the dimension Of drawing	Reject
Major	Component solder defect (missing, extra, wrong component or wrong orientation		Reject
Minor	Component position shift  component soldering pad  X  D  A  A  A  A  A  A  A  A  A  A  A  A	X < 3/4Z Y > 1/3D	Reject Reject
Minor	Component tilt component  soldering pad	Y > 1/3D	Reject
Minor	Insufficient solder component PAD ← PCB	θ <b>≤</b> 20°	Reject

## 3. Metal (Plastic) Frame

Defect	Inspection Item	li	nspection Standa	rds			
Major	Crack / breakage	Any	ywhere	Reject			
		W	L	Acceptable of Scratch			
		w<0.1mm	Any	Ignore			
		0.1 <u>&lt;</u> w<0.2mm	L <u>&lt;</u> 5.0mm	2			
Minor	Frame Scratch	0.2 <u>&lt;</u> w<0.3mm	L <u>&lt;</u> 3.0mm	1			
		w <u>&gt;</u> 0.3mm	Any	0			
		Note: 1. Above criteria applicable to scratch lines with distance greater than 5mm.  2. Scratch on the back side of frame (no visible) can be ignored.					
				Acceptable of Dents / Pricks			
		Φ<	2				
	Frame Dent , Prick	1.0<	1				
Minor	$\Phi = \frac{L + W}{2}$	1.5	0				
	2	Note: 1. Above criteria applicable to any two dents / pricks with distance greater than 5mm 2. Dent / prick on the back side of frame (not visible) can be ignored					
Minor	Frame Deformation	Excee	d the dimension of	drawing			
Minor	Metal Frame Oxidation		Any rust				

#### 4. Flexible Film Connector (FFC)

4. Flexible F Defect		ection Item	Inspection Standards					
Minor	Tilte	d soldering	Within the angle +5°	Acceptable				
Minor	Uneven solder joint /bump			Reject				
			Expose the conductive line	Reject				
Minor	Hole	$\Phi = \frac{L + W}{2}$	Ф> 1.0mm	Reject				
Minor	Position shift		Y > 1/3D	Reject				
IVIII IOI		- <del> </del>	X > 1/2Z	Reject				

### 5. Screw

Defect	Inspection Item	Inspection Standards				
Major	Screw missing/loosen		Reject			
Minor	Screw oxidation	Any rust	Reject			
Minor	Screw deformation	Difficult to accept screw driver	Reject			

### 6. Heatseal \ TCP \ FPC

Defect	Inspection Item	Inspection Standards	
Major	Scratch expose conductive layer		Reject
Minor	HS Hole $\Phi = \frac{L + W}{2}$	⊕> 0.5mm	Reject
Major	Adhesion strength	Less than the specification	Reject
Minor	Position shift	Y > 1/3D	Reject
IVIII IOI	T X	X > 1/2Z	Reject
Major	Conductive line break		Reject

7. LED Backing Protective Film and Others

Defect	Inspection Item	Inspection Standards			
		Acceptable number of units			
		Ф <u>&lt;</u> 0.10mm	Ignore		
		0.10<⊕ <u>&lt;</u> 0.15mm	2		
Minor	LED dirty, prick	0.15<⊕ <u>&lt;</u> 0.2mm	1		
		Ф>0.2mm	0		
		The distance between any two spots should be $\geq$ Any spot/dot/void outside of viewing area is acce			
Minor	Protective film tilt	ilt Not fully cover LCD			
Major	COG coating	Not fully cover ITO circuit			

8. Electric Inspection

Defect	Inspection Item	Inspection Standards	
Major	Short		Reject
Major	Open		Reject

# 9. Inspection Specification of LCD

Defect	Insp	ect Item			Ins	spection	St	andards	•	
		*Glass Scratch	W	,		0.03	0.0	0.0 <u><!--</u--></u>	5 V	V>0.05
		*Polarizer Scratch	L		L٠	<5		L<3		Any
Minor	Linear Defect	st Fiber and Linear	ACC. NO.	1			1 1		Reject	
		material	Note	L is the	ler	-		is the width of the de		efect
		* Foreign material		Φ <u>&lt;</u> 0.		0.1<⊕ <u>&lt;</u> 0	.15 (	).15<⊕ <u>&lt;</u> 0	.2	⊕>0.2
	Black Spot and		ACC. NO.	3EA 100mi	/ m <sup>2</sup>	2		1		0
Minor	Polarizer Pricked	and glass  * Polarizer hole or protuberance by external force	Note	Φ is the average diameter of the defect.  Distance between two defects >10mm.						
		* Unobvious	Φ	(	<b>∆_</b> (	0.3	0.3<	<⊕ <u>&lt;</u> 0.5	0.	5<⊕
	White Spot	transparant foreign material between	ACC. NO.	3EA	/ 10	00mm <sup>2</sup>		1		0
Minor	and Bubble in polarizer	glass and glass or glass and polarizer * Air protuberance between polarizer and glass	Note		$\Phi$ is the average diameter of the defect. Distance between two defects >10mm.					
	Segment Defect		Φ	Ф <b>≤</b> 0.1	10	0.10<⊕ <u>&lt;</u> 0.20		0.20<⊕ <u>&lt;</u> 0.25		Ф>0.25
				3EA 100mr	/ m²	2		1		0
Minor				W is m	ore	than 1/2 s	egme	nt width		Reject
		W.	Note	Note $\Phi = \frac{\Gamma + M}{2}$ Distance between two defect				ect is 10m	m	
				Ф <b>≤</b> 0.1	10	0.10<⊕≤	<0.20 0.20<⊕<0.2		0.25	Ф>0.25
	Protuberant	w W	W	Glue	)	W <u>&lt;</u> 1/2 S W <u>&lt;</u> 0.3			Ignore	
Minor	Segment	$\Phi = (L + W)/2$	ACC. NO.	3EA 100mr	/ m²	2		1		0
			1. Seg	ment	•					
			Е	-				3 <u>&lt;</u> 1.0mm		I.0mm
Minor	Assembly		B-			A<1/2B		A<0.2		<0.25
14111101	Mis-alignment			Judge Acceptable Acceptable Acceptab					ертавіе	
			2. Dot Matrix  Deformation>2° Re					Reject		
Minor	Stain on LCD Panel Surface		or a s	similar	one	ns can be e. Otherwi ck spot" aı	se, ju	udged acc		

# 10. RELIABILITY

No	Item	Condition	Quantity	Criteria
1	High Temperature Operating	70°C, 96Hrs	2	GB/T2423.2 -2008
2	Low Temperature Operating	-20°C, 96Hrs	2	GB/T2423.1 -2008
3	High Humidity	60°C, 90%RH, 96Hrs	2	GB/T2423.3 -2006
4	High Temperature Storage	80°C, 96Hrs	2	GB/T2423.2 -2008
5	Low Temperature Storage	-30°C, 96Hrs	2	GB/T2423.1 -2008
6	Thermal Cycling Test	-20°C, 60min~70°C, 60min, 20 cycles.	2	GB/T2423.22 -2012
7	Packing vibration	Frequency range: 10Hz~50Hz Acceleration of gravity: 5G X,Y,Z 30 min for each direction.	2	GB/T5170.14 -2009
8	Electrical Static Discharge	Air: $\pm 8$ KV 150pF/330 $\Omega$ 5 times	2	GB/T17626.2 -2006
		Contact: $\pm 4$ KV 150pF/330 $\Omega$ 5 times		
9	Drop Test (Packaged)	Height: 80 cm, 1 corner, 3 edges, 6 surfaces.	2	GB/T2423.8 -1995

Note:1) Above conditions are suitable for our company standard products.
2) For restrict products, the test conditions listed as above must be revised.

### 11. HANDLING PRECAUTION

(1) Mounting Method

The panel of the LCD Module consists of two thin glass plates with polarizers which easily getdamaged since the Module is fixed by utilizing fitting holesin the printed circuit board. Extreme care should be taken when handling the LCD Modules.

(2) Caution of LCD handling & cleaning

When cleaning the display surface, use soft cloth with solvent (recommended below) and wipe lightly.

- Isopropyl alcohol
- Ethyl alcohol
- Trichlorotrifloro thane

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface.

Do not use the following solvent:

- Water
- Ketone
- Aromatics

### (3) Caution against static charge

The LCD Module use C-MOS LSI drivers, so we recommend that you connect any unused input terminal to VDD or VSS, do not input any signals before power is turned on. Andground your body, Work/assembly table. Andassembly equipment toprotect against staticelectricity.

#### (4) Packaging

- Modules use LCD elements, and must be treated as such. Avoid intense shock and falls from a height.
- To prevent modules from degradation. Do not operate or store them exposed directly to sunshine or high temperature/humidity.

#### (5) Caution for operation

- It is indispensable to drive LCD's within the specified voltage limit since the higher voltage than the limit shorten LCD life. An electrochemical reaction due to direct current causes LCD deterioration, Avoid the use of direct current drive.
- Response time will be extremely delayed at lower temperature than the operating temperature range and on the other hand at higher temperature LCD's show dark color in them. However those phenomena do not mean malfunction or out of order with LCD's. Which will come back in the specified operating temperature range.
- If the display area is pushed hard during operation, some font will be abnormally displayed but it resumes normal condition after turning off once.
- A slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit.

Usage under the relative condition of 40°C, 50%RH or less is required.

#### (6) Storage

In the case of storing for a long period of time (for instance.) For years) for the purpose or replacement use, The following ways are recommended.

- Storage in a polyethylene bag with sealed so as not to enter fresh air outside in it, And with no desiccant.
- Placing in a dark place where neither exposure to direct sunlight nor light is. Keeping temperature in the specified storage temperature range.
- Storing with no touch on polarizer surface by the anything else. (It is recommended to store them as they have been contained in the inner container at the time of delivery)

#### (7) Safety

- It is recommendable to crash damaged or unnecessary LCD into pieces and wash off liquid crystal by using solvents such as acetone and ethanol. Which should be burned up later.

When any liquid crystal leaked out of a damaged glass cell comes in contact with your hands, please wash it off well with soap and water.

#### (8) Other

- After the product shipped, any product quality issues must be feedback within three months, otherwise, we will not be responsible for the subsequent or consequential events.

